

ABSTRACT

In order to provide a solid state image pickup device in which an offset voltage in an image signal from which a noise signal is removed is made to be low, when an image pickup operation is performed while the MOS transistor T1 is ON, after a signal ϕ_{VD} with a value V_h is integrated by a capacitor C, the signal ϕ_{VD} is V_m ($V_m < V_h$) and a pulse signal ϕ_V is given so that an image signal is output. When dispersion of sensitivity is detected while the MOS transistor T1 is OFF, after the signal ϕ_{VD} with the value V_h is integrated by the capacitor C, the signal ϕ_{VD} is V_l ($V_l < V_m$) and the pulse signal ϕ_V is given so that a noise signal is output.